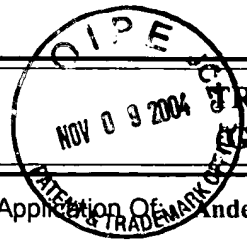


ITW



TRANSMITTAL LETTER (General - Patent Pending)	Docket No. BUR920040173US1
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In Re Application Of **Anderson et al.**

Application No. 10/711,844	Filing Date 10/08/2004	Examiner Not Assigned	Customer No. 46170	Group Art Unit 2811	Confirmation No. 5843
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Title: **INTEGRATED CIRCUIT WITH BULK AND SOI DEVICES CONNECTED WITH AN EPITAXIAL REGION**

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

Supplemental Information Disclosure Statement
PTO-1449
1 associated document
Postcard

in the above identified application.

- ☒ No additional fee is required.
- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **09-0456** as described below.
 - ☐ Charge the amount of _____
 - ☒ Credit any overpayment.
 - ☒ Charge any additional fee required.
- ☐ Payment by credit card. Form PTO-2038 is attached.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.


Signature

Dated: **November 9, 2004**

Marshall M. Curtis
Reg. No. 33,138

Whitham, Curtis & Christofferson, P.C.
11491 Sunset Hills Road, Suite 340
Reston, VA 20190
(703) 787-9400

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____ (Date)
_____ Signature of Person Mailing Correspondence
_____ Typed or Printed Name of Person Mailing Correspondence

CC:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Anderson et al.

Serial No.: 10/711,844

Filed: October 8, 2004

For: INTEGRATED CIRCUIT WITH BULK AND SOI DEVICES CONNECTED
WITH AN EPITAXIAL REGION



Group Art Unit: 2811

Examiner: Unknown

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 C.F.R. 1.97 through 1.99 and pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, applicant respectfully brings the following documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. The documents cited on the attached Form PTO-1449 are in addition to those documents cited on the Form PTO-1449 originally filed with this application on October 8, 2004.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

This statement shall not be construed as a representation that a search has been made or that no other art than that identified exists.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to International Business Machines Corporation deposit account No. 09-0456.

Respectfully submitted,

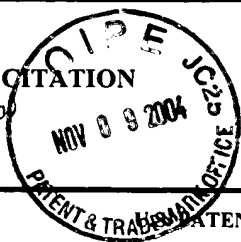
A handwritten signature in black ink, appearing to read "Marshall M. Curtis".

Marshall M. Curtis
Reg. No. 33,138

Whitham, Curtis & Christofferson, P.C.
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Reston, VA 20190
(703) 787-9400
Customer Number 46170

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

BUR920040173US1

Application Number

10/711,844

Applicant(s)

Anderson et al.

Filing Date

10/08/2004

Group Art Unit

2811

PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

		Chan et al.; "On the Integration of CMOS with Hybrid Crystal Orientations"; 2004 Symposium on VLSI Technology, Digest of Technical Papers; pgs. 160-161

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.